|  |  |
| --- | --- |
| **Faculty Names and Abbreviations** | |
| **Name of Faculty** | **Abbreviations** |
|
| Prof. M. T. Beg | MTB |
| Prof. Mainuddin | MU |
| Prof. Sajad Ahmad Loan | SAL |
| Prof. Neelofer Afzal | NA |
| Prof. Dinesh Prasad | DP |
| Prof. M. Nizamuddin | MN |
| Dr. Amber Khan | AK |
| Dr. Md. Waseem Akram | WA |
| Dr. S. Intekhab Amin | SIA |
| Dr. Imran AhmadKhan | IK |
| Dr. Contractural 1 | CT1 |
| Dr. Contractural 2 | CT2 |
| Dr. Guest 1 | GT1 |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |
|  |  |

|  |  |  |  |
| --- | --- | --- | --- |
| **SUBJECT CODE** | **SUBJECT NAME** | **Faculty** | **Room No.** |
| ASB-301  ECS-302  ECS | Engineering Mathematics III (EM-III) | SJ-App. Sc., | 333 |
| ECC-301 | Electronic Devices and Circuits-I (EDC-I) | NA |
| ECC-302 | Circuit Analysis & Synthesis (CAS) | CT1 |
| ECC-303 | Logic Design (LD) | MTB |
| ECC-304 | Signals and Systems (SS) | WA |
| ASM-301 | Universal Human Values (UHV) |  |
| ASM-302 | Essence of Indian Traditional Knowledge | Appl Sci |  |
|  |  |  |  |
| ECC-501  ECS-302  ECS | Active filters and signal processing (AFSP) | DP | 329 |
| ECC-502 | Digital Communication | CT2 |
| ECC-503 | Microprocessor and Application | VLSI-CT1 |
| ECC-504 | Electromagnetic Field Theory | AK |
| ECC-505 | Instrumentation and Control System | CT1 |
| ECE-501 | Antennal and Wave Propagation | GT1 |  |
| ECH-511 | CMOS Digital VLSI Design | MN |  |
| ECD-511 | CMOS Digital VLSI Design | MN |  |
|  |  |  |  |
| ECS-701 | VLSI | SIA | 331 |
| ECS-702 | Wireless Communication (WC) | GT1 |
| ECS-703 | Embedded System Design (ESD) | IK |
| ECS-704 | Digital communication (DC) | CT2 |

|  |  |  |  |
| --- | --- | --- | --- |
| **LAB CODE** | **LAB NAME** | **Faculty** | **Room No.** |
|  |  |  |  |
| ECL-301 | EDC-I | WA(8)/GT1(8) | 252 |
| ECL-302 | Circuit Analysis & Synthesis (CAS) | IK(8) | 245 |
| ECL-303 | Logic Design | AK(8)/CT1 (8) | 214 |
| ECL-304 | Electronic Workshop | VLSI-CT2 (8) | 245 |
|  |  |  |  |
| ECL-501 | Active filters and signal processing (AFSP) | VLSI-CT3(8) /DP(8) |  |
| ECL-502 | Digital Communication | CT2 (8) |  |
| ECL-503 | Microprocessor and Application | VLSI-CT4 (8)/ MN(8) |  |
| ECL-504 | Instrumentation and Control System | CT1 (8) | 258 |
| ECL-521/522 | CMOS Digital VLSI Design | VLSI-CT2 (4) | 257 |
|  |  |  |  |
|  |  |  |  |
| ECS-711 | VLSI | SIAVLSI-CT5 | 246 |
| ECS-712 | Minor project | All Faculty | 245 |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Subject CODE** | **Subject NAME** | **Faculty** | **Name** | **Room No.** |
| MEC-101 | Random Variable & Stochastic Process (RVSP) | Appl. Sc |  | 327 |
| MEC-102 | Low Power VLSI (LP-VLSI) CBCS | WA |  |
| MEC-103 | Advanced Telecommunication & Switching (ATS) | MU | Prof. Mainuddin |
| MEC-104 | Information Theory & Coding (ITC) | AK |  |
|  |  |  |  |  |
| **Lab CODE** | **LAB NAME** | **Faculty** |  |  |
| MEC-151 | Advanced VLSI Lab | VLSI-CT1 |  | 246 |
| MEC-152 | Advanced Communication Lab | CT2 |  | 257 |

|  |  |  |  |
| --- | --- | --- | --- |
| **Subject CODE** | **Subject NAME** | **Faculty** | **Room No.** |
|  |  |  |  |
| MEC-301 | Advanced Signal Processing | SIA | 324 |
| MEC-302 | Modern Digital Communication System | IK |
| **LAB CODE** | **LAB NAME** |  |  |
| MEC-351 | Seminar | All Faculty | 245 |
| MEC-352 | Minor Project | All Faculty |

|  |  |  |  |
| --- | --- | --- | --- |
| **Subject CODE** | **Subject NAME** | **Faculty** | **Room No.** |
|  |  |  |  |
| PHD-101 | Application of 5G in IOT |  |  |
| PHD-102 | Modern Devices and Circuits |  |
|  |  |  |

| Day | Period | I | II | | | III | IV | 01:00-02:00 | V | VI | VII | | | | | VIII |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Time  Class | 9:00-10:00 | 10:00-11:00 | | | 11:00-12:00 | 12:00-01:00 | 02:00-2:50 | 2: 50-03:40 | 03:40-04:30 | | | | | 04:30-05:20 |
| M  O  N | VII | ECS-704: DC CT2 | | | | ECS-702: WC  GT1 | ECS-703: ESD  IK | L  U  N  C  H  B  R  E  A  K | ECS-711: VLSI Lab (X-1) SIA/VLSI-CT5 | | | | | | | |
| ECS-712 : Minor Project (X-2) (NA/DP) | | | | | | | |
| V | ECC-503: MP (VLSI-CT1) | | | | ECC-504: EMFT (AK) | ECH/ECD -511 CDVD (MN) | ECL-501: AFSP Lab (X-1) VLSI-CT3/ DP | | ECL-501: AFSP Lab (X-2) VLSI-CT3/ DP | | | | | |
| ECL-502: Digital Communication Lab (X-2) CT2 | | ECL-502: Digital Communication Lab (X-3) | | | | | |
| ECL-503 MP Lab (X-3) VLSI-CT4/ MN | | ECL-503 MP Lab (X-4) VLSI-CT4/ MN | | | | | |
| ECL-504 ICS Lab (X-4) CT1 | | ECL-504 ICS Lab (X-1) CT1 | | | | | |
| III | ECC-301: EDC-I (NA) | | | | ASM-301: UHV | ASB-301 EM (App Sc.) | \*\*\*\*\*\*\*\*\*\*\* | | | | | | | |
| T  U  E | VII | ECS-701: VLSI (SIA) | | | | ECS-702: WC  GT1 | ECS-703:ESD  IK | ECS 711: VLSI Lab (X-2) (SIA)/ VLSI-CT5 | | | | | | | |
| ECS-712: Minor Project (X-3) (MTB/MU) | | | | | | | |
| V | ECC-505 ICS (CT1) | | | | ECC-503: MP  (VLSI-CT1) | ECH/ECD -511 CDVD (MN) | \*\*\*\*\*\*\*\*\*\*\*\* | | | | | | | |
| III | ECC-303: LD (MTB) | | | | ECC-304: SS  WA | - ASB-301 EM (App Sc.) | ECL-301: EDC-I (X-4) WA/GT1 | | | | | ECL-301: EDC-I (X-3) WA/GT1 | | |
| ECL-302: CAS (X-1) (IK) | | | | | ECL-302: CAS (X-4) (IK) | | |
| ECL-303: Logic Design Lab (X-2)  (AK)/CT1 | | | | | ECL-303: Logic Design Lab (X-1) (AK)/CT1 | | |
| ECL-304 (X-3) VLSI-CT1 | | | | | ECL-304 (X-2) VLSI-CT1 | | |
| W  E  D | VII | ECS-704: DC CT2 | | | | ECS-702: WC  GT1 | ECS-703: ESD  IK | ECS-711: VLSI Lab (X-3) SIA/ VLSI-CT5 | | | | | | | |
| V | ECC-501: AFSP (DP) | | | | ECE-501 AWP (GT1) | | ECL-501: AFSP Lab (X-4) VLSI-CT3/ DP | | | | | | ECL-501: AFSP Lab (X-3) VLSI-CT3/ DP | |
| ECL-502: Digital Communication Lab (X-1) CT2 | | | | | | ECL-502: Digital Communication Lab (X-4) CT2 | |
| ECL-503 MP Lab (X-2) VLSI-CT4/ MN | | | | | | ECL-503 MP Lab (X-1) VLSI-CT4/ MN | |
| ECL-504 ICS Lab (X-3) CT1 | | | | | | ECL-504 ICS Lab (X-2) CT1 | |
| III | ECC-303: LD (MTB) | | | ECC-301: EDC-I (NA) | ECC 302:  CAS (CT1) | ASB-301 EM (App Sc.) | \*\*\*\*\*\*\*\*\*\*\* | | | | | | | |
| T  H  U | VII | ECS-701: VLSI (SIA) | | | | ECS-702: WC  GT1 | ECS-703: ESD  IK | ECS-712: Minor Project (X-1) AK/IK | | | | | | | |
| V | ECC-504: EMFT (AK) | | | | ECS-501: AFSP (DP) | ECH/ECD -511 CDVD (MN) | \*\*\*\*\*\*\*\*\*\*\*\*\* | | | | | | | |
| III | ECC-302: CAS (CT1) | | | | ASM-301: UHV | | ECL-301: EDC-I (X-1) WA/GT1 | | | ECL-301: EDC-I (X-2) WA/GT1 | | | | |
| ECL-302: CAS (X-2) (IK) | | | ECL-302: CAS (X-3) (IK) | | | | |
| ECL-303: Logic Design Lab (X-3)  (AK)/CT1 | | | ECL-303: Logic Design Lab (X-4) (AK)/CT1 | | | | |
| ECL-304 (X-4) VLSI-CT1 | | | ECL-304 (X-1) VLSI-CT1 | | | | |
| F  R  I | VII | ECS-712 : Minor Project (X-2) MN/ WA | | | | | | \*\*\*\*\*\*\*\*\*\*\* | | | | | | | |  |
| ECS-712: Minor Project (X-3) (MTB/MU) | | | | | | | |
| V | ECC-502: DC (CT2) | | ECC-505: ICS (CT1) | | ECC-502: DC CT2 | | ECL-521/522 CMOS Minor/Honors Lab (X-1) VLSI-CT4 | | | | ECL-521/522 CMOS Minor/Honors Lab (X-2) VLSI-CT4 | | | |
| III | ECC-304: SS WA | | | |  | |  | | | | | | | |

| **Day** | **Period** | **I** | **II** | **III** | **IV** | **01:00-02:00** | **V** | **VI** | **VII** | | | | | **VIII** |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Time**  **Class** | **9:00-10:00** | **10:00-11:00** | **11:00-12:00** | **12:00-01:00** | **02:00-3:00** | **03:00-04:00** | **04:00-05:00** | | | | | **05:00-06:00** |
| **M**  **O**  **N** | **III-Sem** | MEC-351 IK | | | MEC-301 SIA | MEC-352 MU | | | MEC-352 SIA | | | | |
| **I-Sem** | MEC-103: ATS (MU) | | MEC- 101 RVSP (App.Sc) | MEC-102  LP-VLSI (WA) |  | MEC-151: Advanced VLSI lab VLSI-CT1 | | | | | | | |
| **T**  **U**  **E** | **III-Sem** | MEC-302 IK | | xxxxxx | MEC-301 SIA | MEC-351 MTB | | | | | | | MEC-352 IK |
| I-Sem | MEC-103: ATS (MU) | | MEC-101: RVSP (App.Sc) | MEC-102  LP-VLSI (WA) | xxxxxxx | | | | | | | |
| **W**  **E**  **D** | **III-Sem** | MEC-302 IK | | xxxxxxx | MEC-301 SIA | MEC-352 DP | | | | MEC-352 IK | | | |
| I-Sem | MEC-101 RVSP (App.Sc) | | xxxxx | MEC-102  LP-VLSI (WA) | xxxxxxx | | | | | | | |
| **T**  **H**  **U** | **III-Sem** | MEC-351 MN | | | MEC-301 SIA | MEC-352 WA | | | | | MEC-352 SIA | | |
| I-Sem | MEC-104: ITC () | | xxxxx | MEC-102  LP-VLSI (WA) |  | | | | | | | |
| **F**  **R**  **I** | **III-Sem** | MEC-351 AK | | | xxxxxxx | MEC-352 IK | | | | | | MEC-352 NA | |
| I-Sem | MEC-104: ITC | | xxxxx | xxxxxx |  | MEC-152: Advanced Communication Lab CT2 | | | | | | | |

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Day** | **Period** | **I** | **II** | **III** | **IV** |  | **V** | **VI** | **VII** | **VIII** |
| **Time** | **09:00-10:00** | **10:00-11:00** | **11:00-12:00** | **12:00-01:00** | **01:00-02:00** | **02:00-03:00** | **3:00-04:00** | **04:00-05:00** | **05:00-06:00** |
| **M**  **O**  **N** | **I SEM** | PHD-101 () | |  |  |  |  | | | |
| **T**  **U E** | **I SEM** | PHD-101 () | |  |  |  | | | |
| **W**  **E**  **D** | **I SEM** | PHD-102 () | |  |  |  | | | |
| **T**  **H**  **R** | **I SEM** | PHD-102 () | |  |  |  | | | |
| **F**  **R**  **I** |  | \*\*\*\*\*\*\*\* | | |  |  | | | |

PHD-101: Application of 5G in IOT

PHD-102: Modern Devices and Circuits